

## FIG.1 PRIOR ART

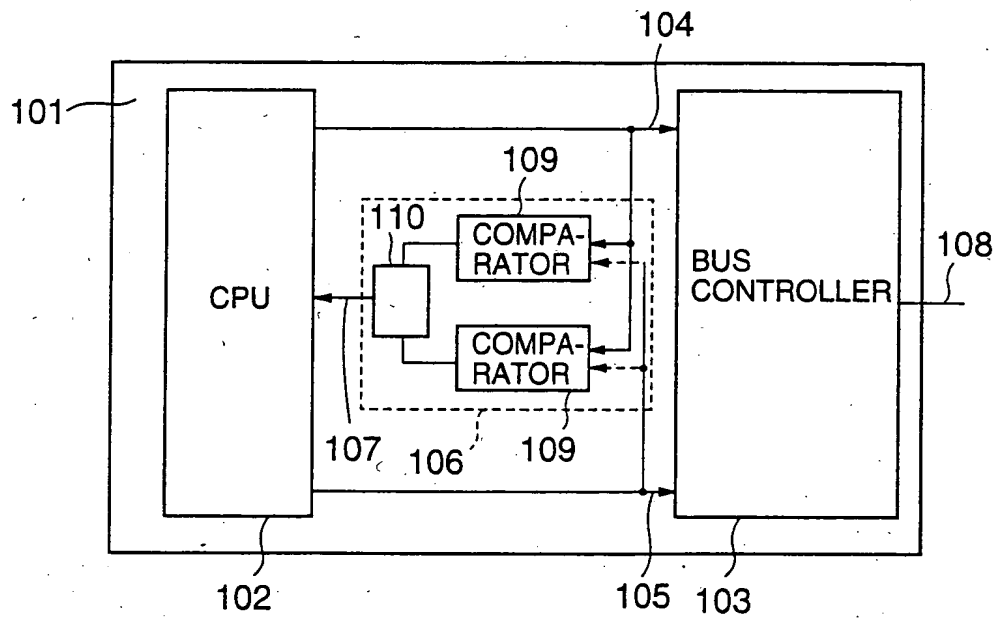


FIG.2

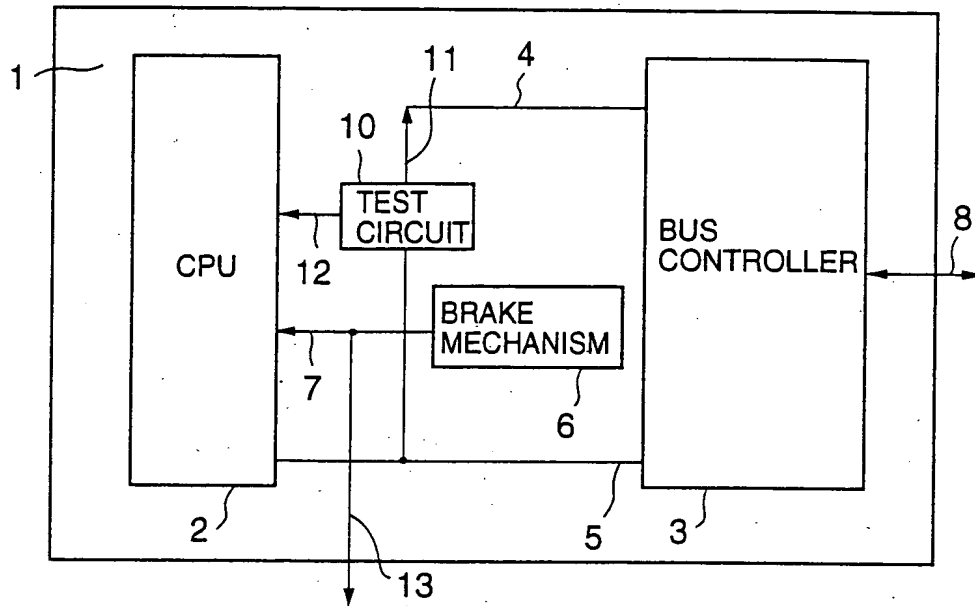


FIG.3

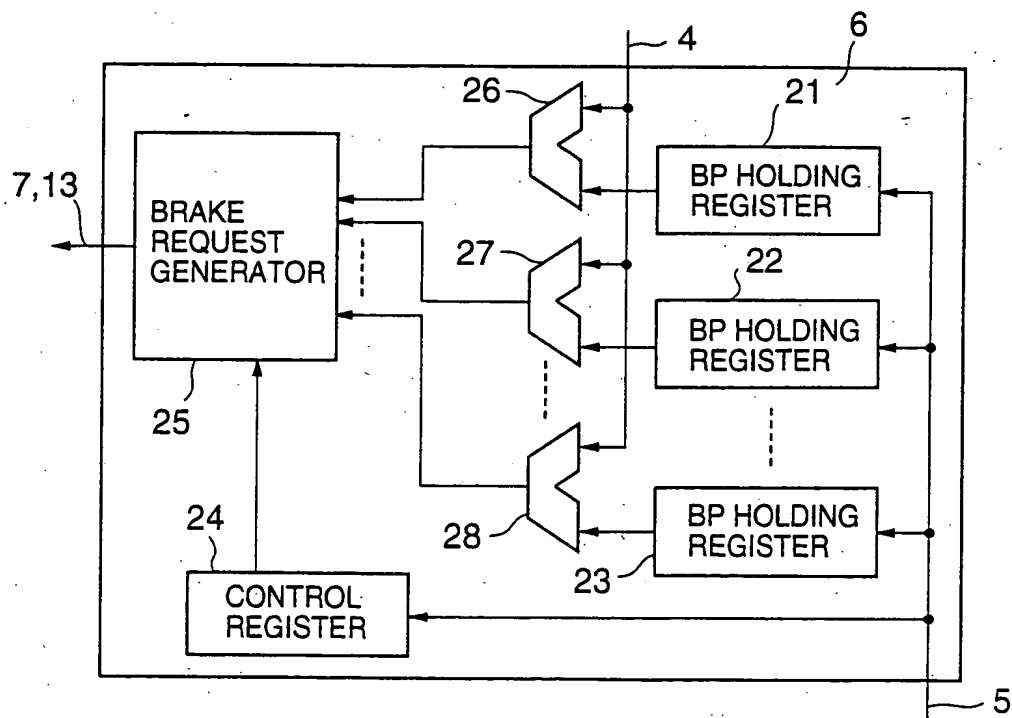


FIG.4

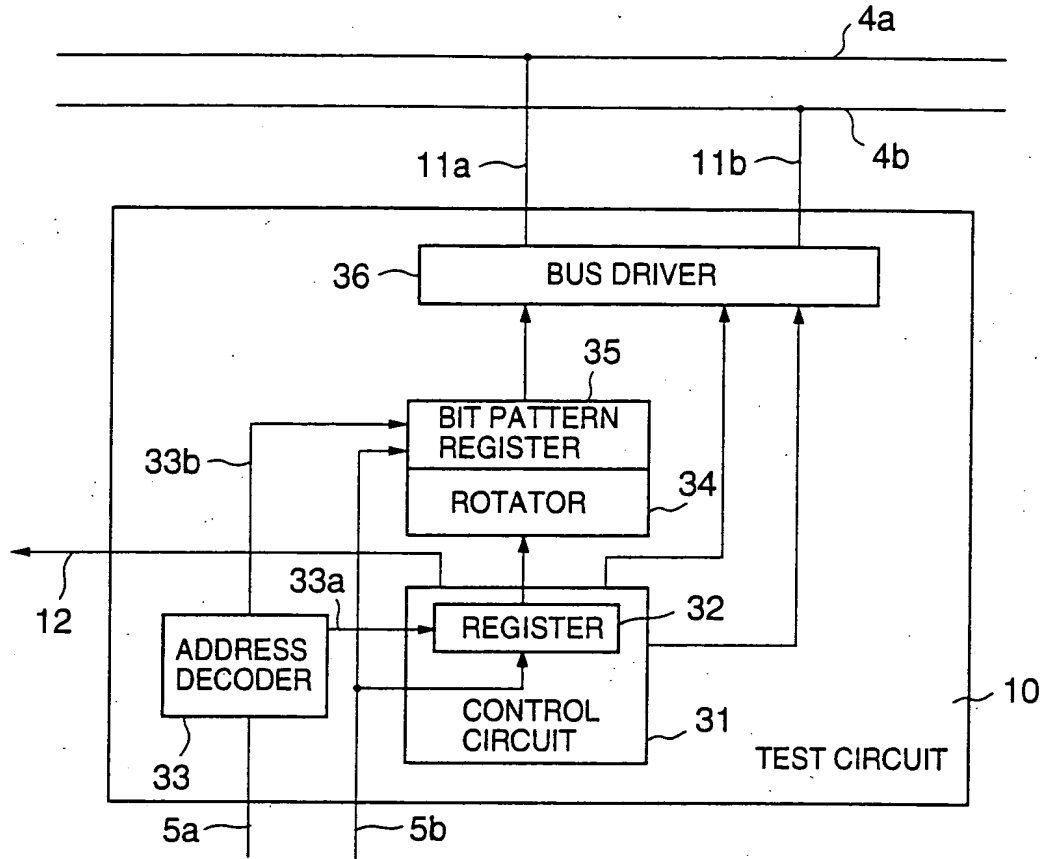


FIG.5

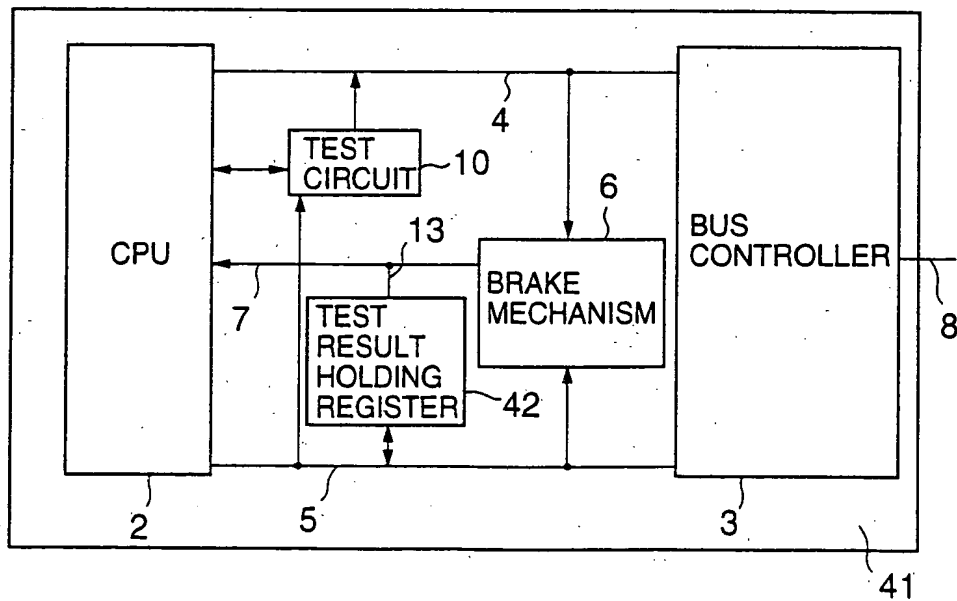


FIG.6

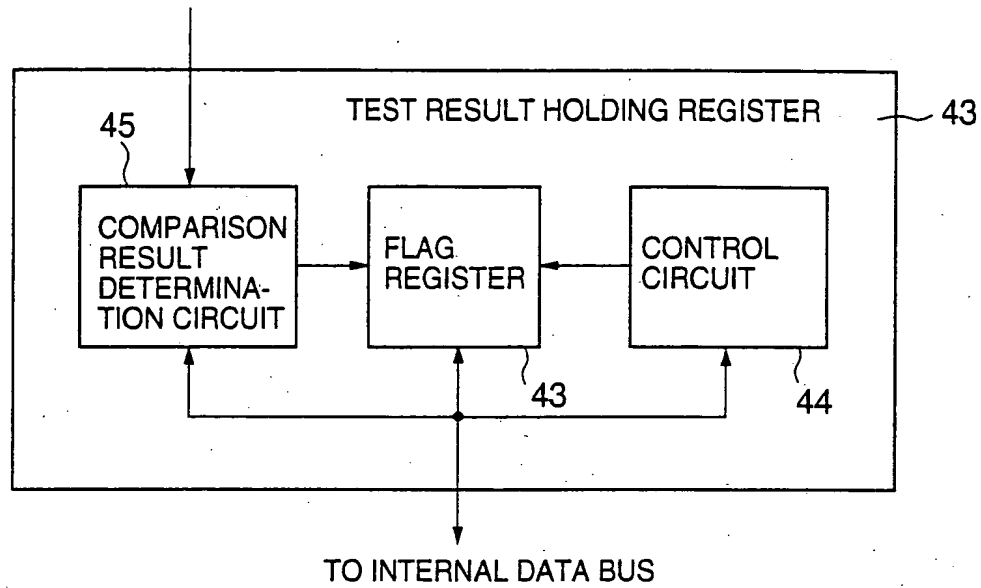


FIG.7

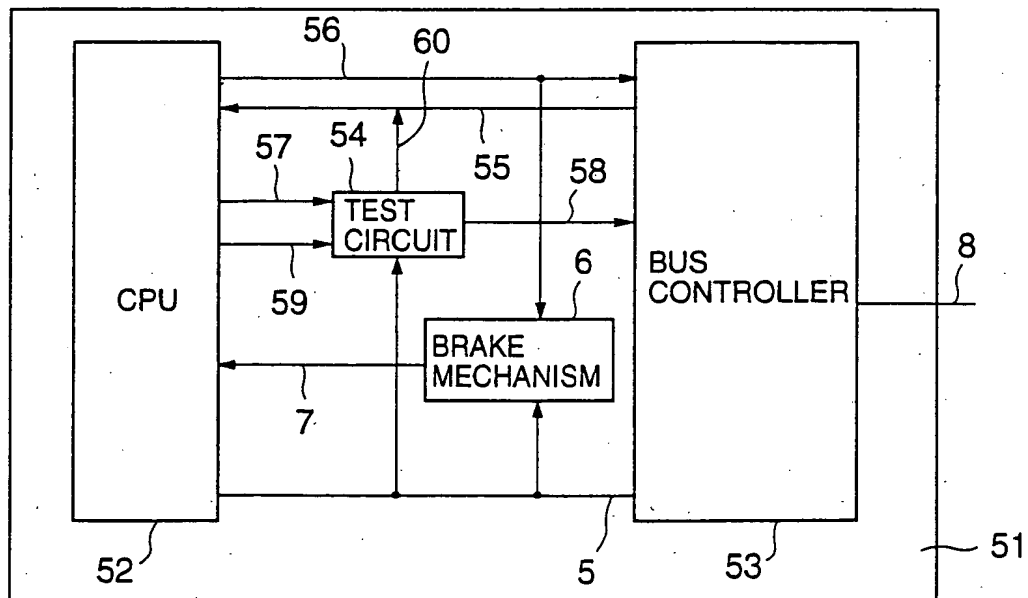


FIG.8

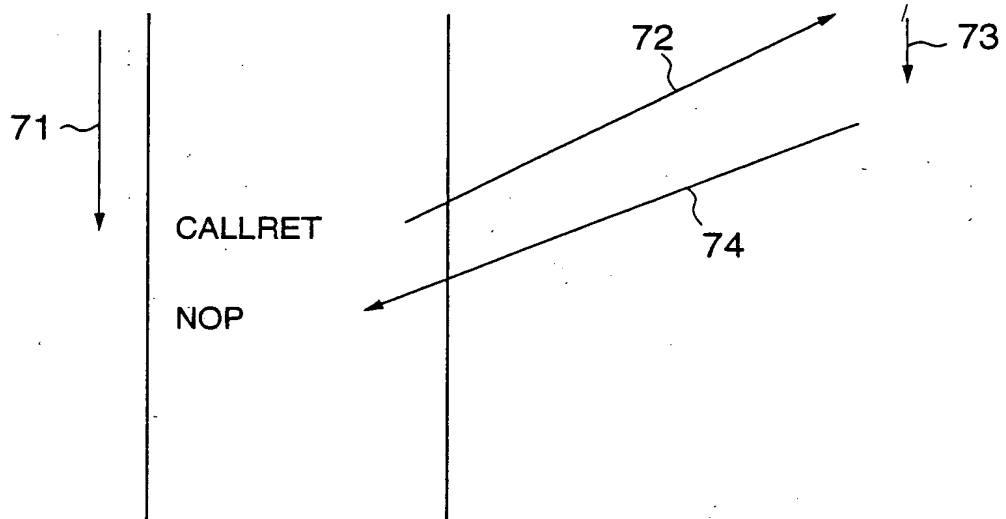


FIG.9

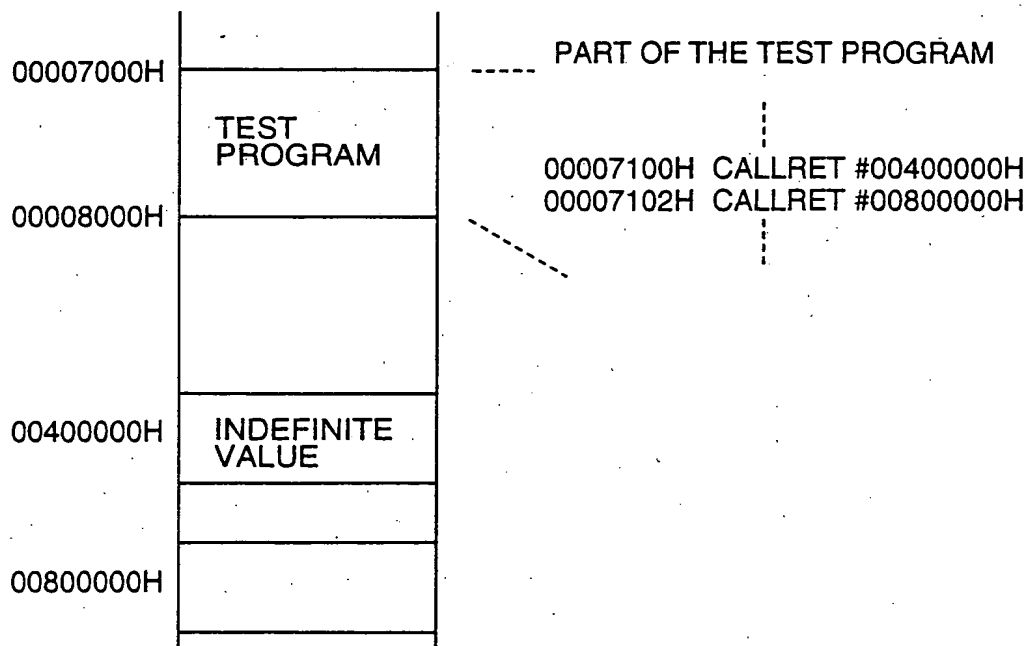


FIG.10

100	INSTRUCTION#1
101	INSTRUCTION#2
102	STIA
103	INSTRUCTION#3
104	INSTRUCTION#4
105	INSTRUCTION#5

FIG.11

T0  
↓

IF	#1	#2	STIA	#3	#4	#200	#5		
ID		#1	#2	STIA	#3	#4	#200	#5	
EX			#1	#2	STIA	#3	#4	#200	#5
MA				#1	#2	STIA	#3	#4	NOP

FIG.12

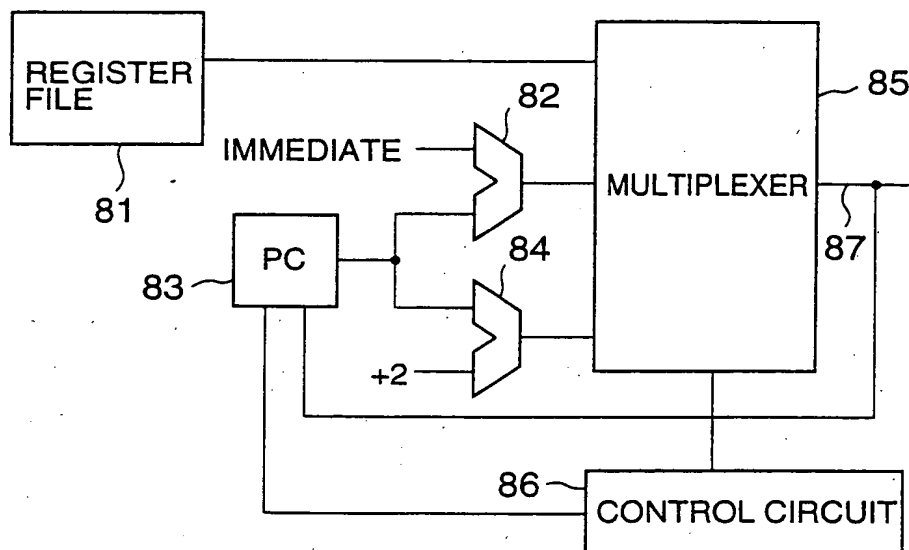


FIG.13

T0    T1    T2    T3  
↓    ↓    ↓    ↓

IF	#1	#2	STIA	#3	#4	#200	#400	#800	#1600	#5	#6
ID		#1	#2	STIA	#3	#4	#4	#4	#4	NOP	#5
EX			#1	#2	STIA	#3	#3	#3	#3	#4	NOP
MA				#1	#2	STIA	STIA	STIA	STIA	#3	#4

FIG.14

R0	00001000
R1	00002000
R2	00004000
R3	00008000
R4	00010000
⋮	⋮

FIG.15

